

Attorney's Docket No. 42390P2445C3

#3  
PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of: )

Alexander D. Peleg et al. )

Examiner: Not Yet Assigned

Application No.: Not Yet Assigned )

Art Unit: Not Yet Assigned

Filed: Herewith )

For: METHOD AND APPARATUS )  
FOR PERFORMING MULTIPLY- )  
ADD OPERATIONS ON PACKED )  
DATA )

Which is a Continuation of: )

Application No.: 08/960,413 )

Filing Date: October 29, 1997 )

Which is a Continuation of: )

Application No.: 08/551,196 )

Filing Date: October 31, 1995 )

Which is a Continuation of: )

Application No.: 08/522,067 )

Filing Date: August 31, 1995 )

Assistant Commissioner for Patents  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

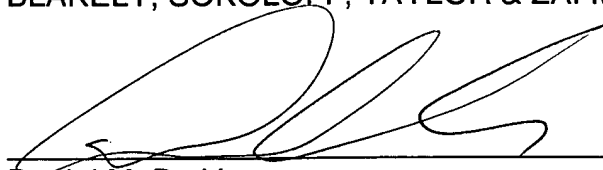
- ☒ 37 C.F.R. §1.97(b).
- ☐ 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:
- ☐ A statement pursuant to 37 C.F.R. §1.97(e) or
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If there are any additional charges, please charge Deposit Account No.  
02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 11/19, 2001

  
Daniel M. De Vos  
Reg. No. 37,813

12400 Wilshire Blvd.  
Seventh Floor  
Los Angeles, CA 90025-1026  
(408) 720-8300

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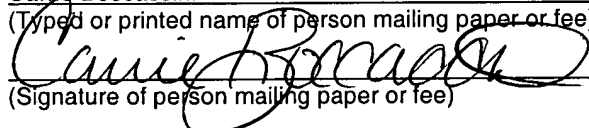
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11-19-2001

(Date signed)

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 42390P2445C3	Application Number: Not Yet Assigned
Title: A METHOD AND APPARATUS FOR PERFORMING MULTIPLY-ADD OPERATIONS ON PACKED DATA		First Named Inventor: Alexander D. Peleg	
		Filing Date: Herewith	Page 1 of 4

JC828 U.S. PTO  
09/989736  
11/19/01

### U.S. PATENT DOCUMENTS

Exam. Initial*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (If known)			
		6,035,316		Peleg, et al.	3/7/2000	
		5,983,256		Peleg, et al.	11/9/1999	
		5,859,997		Peleg, et al.	1/12/1999	
		5,793,661		Dulong, et al.	8/11/1998	
		5,721,892		Peleg, et al.	2/24/1998	
		5,576,983		Shiokawa	11/19/1996	
		5,528,529		Seal	6/18/1996	
		5,517,438		Dao-Trong, et al.	5/14/1996	
		5,509,129		Guttag, et al.	4/16/1996	
		5,506,865		Weaver, Jr.	4/9/1996	
		5,487,022		Simpson, et al.	1/23/1996	
		5,457,805		Nakamura	10/10/1995	
		5,442,799		Murakami, et al.	8/15/1995	

### FOREIGN PATENT DOCUMENTS

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (If known)				

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). <sup>4</sup>For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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		Number	Kind Code <sup>2</sup> (If known)			
		5,420,815		Nix, et al.	5/30/1995	
		5,325,320		Chiu	6/28/1994	
		5,321,644		Schibinger	6/14/1994	
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		4,771,379		Ando, et al.	9/13/1988	

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		4,707,800		Montrone, et al.	11/17/1987	
		4,498,177		Larson	2/5/1985	
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		4,393,468		New	7/12/1983	
		4,344,151		White	8/10/1982	
		4,161,784		Cushing, et al.	7/17/1979	
		3,723,715		Chen, et al.	3/27/1973	
		3,711,692		Batcher	1/16/1973	

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<b>OTHER ART - NO PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation <sup>2</sup>
		International Search Report for PCT/US96/12799, Dated 11-26-1996 (1 page)	
		J. SHIPNES, <u>Graphics Processing with the 88110 RISC Microprocessor</u> , IEEE (1992), pp 169-174.	
		<u>MC88110 Second Generation RISC Microprocessor User's Manual</u> , Motorola Inc. (1991)	
		<u>Errata to MC88110 Second Generation RISC Microprocessor User's Manual</u> , Motorola Inc. (1992) pp 1-11	
		<u>MC88110 Programmer's Reference Guide</u> , Motorola Inc. (1992), pp 1-4.	
		<u>i860™ Microprocessor Family Programmer's Reference Manual</u> , Intel Corporation (1992), Ch. 1, 3, 8, 12.	
		R.B. LEE, <u>Accelerating Multimedia With Enhanced Microprocessors</u> , IEEE Micro (April 1995), pp 22-32.	
		<u>TMS320C2X User's Guide</u> , Texas Instruments (1993) pp3-2 through 3-11; 3-28 through 3-34; 4-1 through 4-22; 4-41; 4-103; 4-119 through 4-120; 4-150 through 4-151.	
		L. GWENNAP, <u>New PA-RISC Processor Decodes MPEG Video</u> , Microprocessor Report (Jan. 1994), pp 16, 17.	
		SPARC Technology Business, <u>Ultra SPARC Multimedia Capabilities On-Chip Support for Real-Time Video and Advanced Graphics</u> , Sun Microsystems (Sept. 1994)	
		Y. KAWAKAMI et al., <u>LSI Applications: A Single-Chip Digital Signal Processor for Voiceband Applications</u> , Solid State Circuits Conference, Digest of Technical Papers; IEEE International (1980)	
		B. CASE, <u>Philips Hopes to Displace DSPs with VLIW</u> , Microprocessor Report (Dec. 1994), pp 12-18.	
		N. MARGULIS, <u>i860™ Microprocessor Architecture</u> , McGraw Hill, Inc. (1990), Ch. 6, 7, 8, 10, 11.	
		<u>Pentium Processor User's Manual, Volume 3: Architecture and Programming Manual</u> , Intel Corporation (1993), Ch. 1, 3, 4, 6, 8, and 18.	
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